

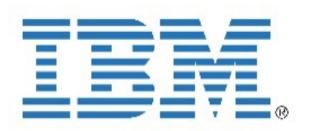


# Covert Message Channels and Clock Spoof DoS Attacks on IEEE Precision Time Protocol (PTPv2) with Timemaster

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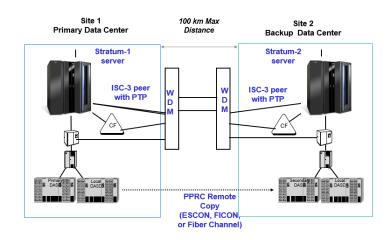






#### Overview - What is PTP?

- The IEEE 1588 standard Precision Time Protocol standard (PTP) is a follow-on to the well known Network Time Protocol (NTP) which provides highly accurate (nanosecond or better) synchronized data center clock signals.
- Cyberattacks which destroy clock synchronization have devastating consequences.
  - Does not preserve order of transactions; critical issue for IBM Z Systems and Next Generation GDPS
  - Impacts event scheduling (backup/recovery with incorrect timestamps) including recovery time point/objective, causality violation
  - Induce time skips, temporal vortex, or complete loss of clock synchronization to all clients

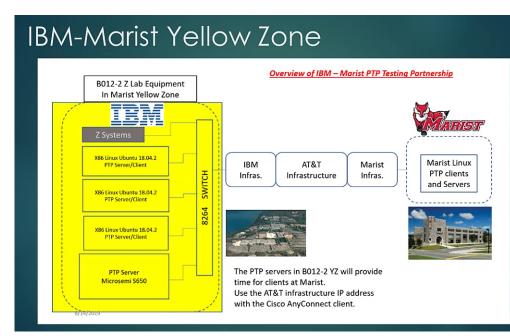






## The IBM-Marist Joint Study Program

- We built an experimental test bed through the IBM-Marist Joint Study. Results include: security vulnerabilities in PTP, and propose mitigation techniques for several attacks:
  - 1. DoS Attack using Announce packets
  - 2. Source Spoof Attack (masquerade attack impersonating system source clock)
  - 3. Atomic Source Takeover (spoof the PTP process with a fake atomic clock)
  - 4. Covert Channel MITM Attack
  - 5. Clock Frequency Manipulation Attack
  - PTP Covert Channel for data exfiltration

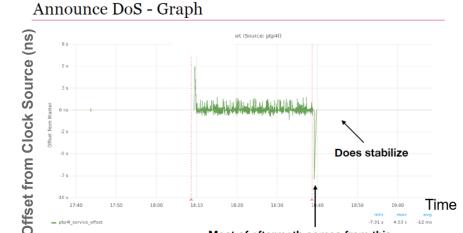


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#### 1. Announce DoS – spam announce packets at the follower

#### Announce DoS 192.168.1.1 224.0.1.129 PTPv2 63854 106 Announce Message 192.168.1.1 PTPv2 224.0.1.129 55201 106 Announce Message 192.168.1.1 224.0.1.129 PTPv2 55200 106 Announce Message 192.168.1.1 224.0.1.129 PTPv2 55199 106 Announce Message 192.168.1.1 224.0.1.129 PTPv2 55198 106 Announce Message 192.168.1.1 224.0.1.129 PTPv2 55197 106 Announce Message 192.168.1.1 224.0.1.129 PTPv2 55196 106 Announce Message 86 Delay\_Req Message 192.168.1.3 224.0.1.129 PTPv2 3177 192.168.1.1 PTPv2 106 Announce Message 224.0.1.129 55195 192,168,1,1 224.0.1.129 PTPv2 55194 106 Announce Message 192.168.1.1 PTPv2 224.0.1.129 55193 106 Announce Message 192.168.1.1 224.0.1.129 PTPv2 55051 106 Announce Message 192.168.1.1 224.0.1.129 PTPv2 55050 106 Announce Message



Most of aftermath comes from this

No need to spoof sequence IDs

ptp4l service offset

200-300 spam packets/second

"Valid" Sequence IDs

Average Offset During Attack: 137.8 ms

Average Offset After Attack: -86.1 ms

Spoofed IP

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2. Source Spoof – pretend to be the main clock source and send false data to the followers



30 minute attack can push the clock days or years out of sync

We do not need to know the IP address of the follower since multicast is supported; the multicast address (224.0.1.129) and port (320) always remain the same.

The clock ID of the follower is not required.

We only need to know the MAC address of the PTP enabled switch.

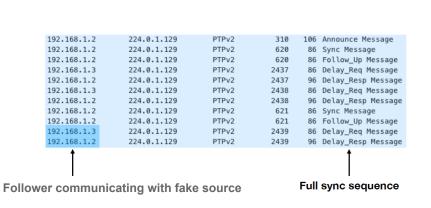
Although the follower recognizes that something is wrong (as reflected in the syslog and management console logs), it still accepts our spoofed SYNC packets.

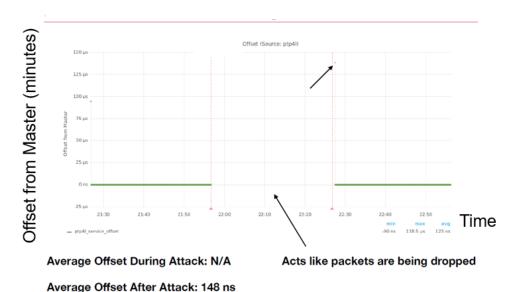
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fake the whole PTP process and pretend to be an atomic clock





6



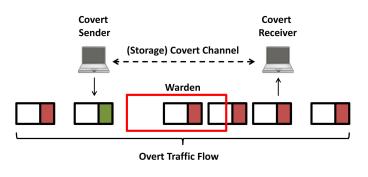
## **Possible Mitigation So Far...**

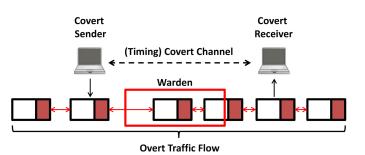
- Attacks succeed because there is no authentication between the follower and the clock source
  - No session semantics; follower accepts any packets with a valid sequence number from a reasonable looking IP address
- Construct binding between the source clock ID and its IP address
  - Follower can derive the source clock address and verify the source
- Add a nonce to the source clock sequence numbers to uniquely verify each series of timing packets
- Establish digital identity for clock source (FPA with TAC, see IEEE Trans.)
- Consider NTS for PTP, Tesla, other emerging options





#### **Covert Channels**





- Covert channels transfer information between processes that are not normally allowed to communicate based on cybersecurity policy
- Ideally the communication is difficult to detect by other processes unless all meta-data fields are validated, and does not obviously impede normal operation
- Covert channels were not designed for communication, and therefore often exhibit low data rates, lack of redundancy/retransmission or error correction capability
- Often used for data exfiltration or to install/update malware
- Prior documented examples include DNS, NTP, and others (see N. Tsapakis, Virusbulletin.com, April 2019)





#### PTP Packet Headers as Covert Channels

```
Precision Time Protocol (IEEE1588)

□ 0000 .... = transportSpecific: 0x00

     ...0 .... = V1 Compatibility: False
    .... 0001 = messageId: Delay_Req Message (0x01)
    .... 0010 = versionPTP: 2
   messageLength: 44
   subdomainNumber: 0

¬ flags: 0x0000

     0... ---- = PTP_SECURITY: False
     .0.. .... = PTP profile Specific 2: False
     ..... = PTP profile Specific 1: False
     .... .0.. .... = PTP_UNICAST: False
     .... ..0. .... = PTP_TWO_STEP: False
     .... ...0 .... = PTP_ALTERNATE_MASTER: False
     .... - .... - FREQUENCY_TRACEABLE: False
      .... .... ...0 .... = TIME_TRACEABLE: False
      .... .... 0... = PTP_TIMESCALE: False
      .... .... .0.. = PTP UTC REASONABLE: False
     .... .... .... ..0. = PTP_LI_59: False
     .... .... .... 0 = PTP_LI_61: False
  rcorrection: 59345.000000 nanoseconds
     correction: Ns: 59345 nanoseconds
     correctionSubNs: 0.000000 nanoseconds
   clockIdentity: 0x001d9cfffeb1acfe
    SourcePortID: 1
    sequenceId: 15638
   control: Delay_Req Message (1)
   logMessagePeriod: 127
   originTimestamp (seconds): 1436270274
   originTimestamp (nanoseconds): 26902220
```

- 1. Sniff for incoming packets to determine the next sequence ID (only to avoid packet collision).
- Construct spoofed packet.
  - 8 bytes is inserted into the correction field during packet creation.
  - 2. 8 bytes can also optionally be inserted into the clock identity field.
- 3. Read hexadecimal data from a text file to simulate data exfiltration.
- Send spoofed packet to source node.
- 5. Send packets in time intervals that mimic normal occurrences.

Undetectable for delay\_request messages





#### **Covert Channel Effects on PTP**

- Node not running ptp: The source node responds normally to the delay req messages with a delay response
- **Node running ptp with non-colliding sequence ids**: Same reaction as a node that's not running ptp; normal response, undetected (this is part of the default IBM Enterprise Profile for PTP)
- Node running ptp with colliding sequence ids: The data in the correction field is reflected by the raw delay value in ptp output. Source still sends delay response
- Node running ptp with colliding sequence ids & tsproc\_mode set to raw: Large source offsets since the
  offset is now computed taking into account the raw value

Leads to 2 new attack vectors...





#### 4. Correction Field MITM Attack

 Intercepting packets before they leave the boundary node, injecting large data into the correction field to cause large offset at the follower node

• Results were...unexpected...

Switch

Clock Source
192.168.1.1

Follower
192.168.1.3

192.168.1.2





#### **MITM Attack Results**

 If correction field values are too small, we get negative delay messages but PHC2SYS still runs fine

```
ptp4l[620564.317]: negative delay
ptp4l[620564.317]: delay = (t2 - t3) * rr + (t4 - t1)
ptp4l[620564.317]: t2 - t3 = -186474202174
ptp41[620564.317]: t4 - t1 = +186474201949
ptp4l[620564.317]: rr = 1.0000000000
ptp4l[620564.317]: delay filtered
                                                           -112
ptp4l[620564.810]: port 1: delay timeout
ptp4l[620564.810]: negative delay
ptp41[620564.810]: delay = (t2 - t3) * rr + (t4 - t1)
ptp4l[620564.810]: t2 - t3 = -186967257005
ptp4l[620564.810]: t4 - t1 = +186967256724
ptp4l[620564.810]: rr = 1.0000000000
ptp4l[620564.810]: delay filtered
                                           105 raw
                                                           -140
ptp4l[620566.391]: port 1: delay timeout
ptp4l[620566.391]: negative delay
ptp41[620566.392]: delay = (t2 - t3) * rr + (t4 - t1)
ptp41[620566.392]: t2 - t3 = -188548883971
ptp41[620566.392]: t4 - t1 = +188548883515
ptp41[620566.392]: rr = 1.0000000000
ptp41[620566.392]: delay filtered
                                           51 raw
                                                           -228
ptp4l[620566.475]: port 1: delay timeout
ptp41[620566.475]: negative delay
ptp41[620566.475]: delay = (t2 - t3) * rr + (t4 - t1)
ptp41[620566.475]: t2 - t3 = -188632874595
ptp41[620566.475]: t4 - t1 = +188632874130
ptp41[620566.475]: rr = 1.0000000000
ptp41[620566.476]: delay filtered
                                           -28 raw
```

 If correction field values are too large, we stop getting offset messages at all; impossible to graph offsets or tell how this attack impacts PTP4L

Intn/1[610220 030]: nort 1: delay timeout

hrh41[019553.939]:	port 1:	delay timeout			
ptp4l[619229.940]:	,	filtered	2342	raw	2342
ptp4l[619230.942]:	port 1:	delay timeout			
ptp4l[619230.942]:	delay	filtered	2342	raw	2345
ptp4l[619231.046]:	port 1:	delay timeout			
ptp4l[619231.046]:	delay	filtered	2342	raw	2345
ptp4l[619232.838]:	port 1:	delay timeout			
ptp4l[619232.838]:	delay	filtered	2342	raw	2347
ptp4l[619233.331]:	port 1:	delay timeout			
ptp4l[619233.331]:	delay	filtered	2343	raw	2348
ptp4l[619234.991]:	port 1:	delay timeout			
ptp4l[619234.991]:	delay	filtered	2344	raw	2350
ptp4l[619235.260]:	port 1:	delay timeout			
ptp4l[619235.261]:	delay	filtered	2345	raw	2351
ptp4l[619236.752]:	port 1:	delay timeout			
ptp4l[619236.753]:	delay	filtered	2346	raw	2358
ptp4l[619237.465]:	port 1:	delay timeout			
ptp4l[619237.465]:	delay	filtered	2347	raw	2360
ptp4l[619238.362]:	port 1:	delay timeout			
ptp4l[619238.363]:	delay	filtered	2349	raw	2360
ptp4l[619239.516]:	port 1:	delay timeout			
ptp4l[619239.517]:	delay	filtered	2350	raw	2359
ptp4l[619239.922]:	port 1:	delay timeout			





# 5. Clock Frequency Manipulation Attack

- Spoof packets with large amounts of data in correction field
- Clock frequency exceeds max value, unable to synchronize with source

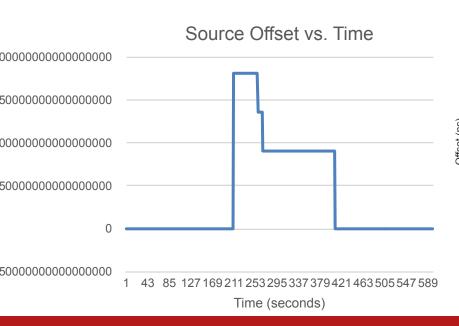
```
ptp41[96199.504]: master offset 814429228880942183 s2 freq
                                                               -nan bath delay 814429218391406124
                                                                                                     ptp41[96534.372]: elockcheck: clock jumped backward or running slower than expected
                                                                                                     ptp41[96534.377]: master offset -3171763 s0 freq
ptp4l[96199.505]: master offset 814429228881957607 s2 freq
                                                               -nan ath delay 814429218391406124
                                                                                                                                                          -nan path delay
                                                                                                                                                                              6992
                                                                                                     ptp4l[96534.576]; port 1; delay timeout
ptp4l[96199.510]: master offset 814429228886727911 s2 freq
                                                               -nan path delay 814429218391406124
ptp4l[96199.512]: master offset 814429228887743463 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp41[96534.576]: delay filtered
ptp4l[96199.516]: master offset 814429228891974503 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp4l[96535,396]: port 1: delay timeout
ptp4l[96199.517]: master offset 814429228892993511 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp41[96535.397]: delay filtered
                                                                                                                                                              9184
                                                                                                     ptp41[96535.473]: clockcheck: clock jumped backward or running slower than expected!
                                                               -nan path delay 814429218391406124
ptp4l[96199.522]: master offset 814429228897199847 s2 freq
                                                                                                     ptp4l[96535.473]: master offset -3180595 s0 freq
                                                                                                                                                          -nan path delay
                                                               -nan path delay 814429218391406124
ptp4l[96199.523]: master offset 814429228898218727 s2 freq
                                                                                                     ptp4l[96536.213]: port 1: delay timeout
                                                               -nan path delay 814429218391406124
ptp4l[96199.528]: master offset 814429228902661735 s2 freq
                                                                                                     ptp4l[96536.213]: delay filtered
                                                                                                                                             6992 raw
ptp4l[96199.529]: master offset 814429228903680231 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp4l[96536.573]: clockcheck: clock jumped backward or running slower than expected!
                                                               -nan path delay 814429218391406124
ptp4l[96199.534]: master offset 814429228907889255 s2 freq
                                                                                                     ptp4l[96536.573]: master offset -3189491 s0 freq
                                                                                                                                                          -nan path delav
                                                                                                                                                                               6992
ptp4l[96199.535]: master offset 814429228908909159 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp41[96537.673]: clockcheck: clock jumped backward or running slower than expected!
ptp4l[96199.539]; master offset 814429228913116391 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp4l[96537,673]: master offset -3198323 s0 frea
                                                                                                                                                          -nan path delay
ptp4l[96199.541]: master offset 814429228914133159 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp4l[96537.675]: port 1: delay timeout
ptp4l[96199.545]: master offset 814429228918348519 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp41[96537.675]: delay filtered
                                                                                                                                                              5056
ptp4l[96199.546]: master offset 814429228919375335 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp41[96538.249]: port 1: delay timeout
ptp4l[96199.551]: master offset 814429228923794727 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp41[96538.249]: delay filtered
                                                                                                                                             7360
ptp4l[96199.552]: master offset 814429228924813735 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp41[96538.773]: clockcheck: clock jumped backward or running slower than expected!
                                                               -nan path delay 814429218391406124
ptp4l[96199.557]: master offset 814429228929037799 s2 freq
                                                                                                     ptp41[96538.773]: master offset -3207587 s0 freq
                                                                                                                                                          -nan path delay
                                                                                                                                                                              7360
ptp4l[96199.558]: master offset 814429228930058855 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp4l[96539.207]: port 1: delay timeout
ptp4l[96199.563]: master offset 814429228934266727 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp41[96539.208]: delay filtered
ptp4l[96199.564]: master offset 814429228935297255 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp41[96539.873]: clockcheck: clock jumped backward or running slower than expected!
ptp4l[96199.569]: master offset 814429228939722471 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp41[96539.873]: master offset -3216051 s0 freq
                                                                                                                                                          -nan path delay
ptp4l[96199.570]: master offset 814429228940744615 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp41[96540.973]: clockcheck: clock jumped backward or running slower than expected!
ptp4l[96199.574]: master offset 814429228944960103 s2 freq
                                                               -nan path delay 814429218391406124
                                                                                                     ptp4l[96540.974]: master offset -3224947 s0 freq
                                                                                                                                                          -nan path delay
                                                                                                                                                                              7056
                                                               -nan path delay 814429218391406124
ptp4l[96199.576]: master offset 814429228945980007 s2 freq
                                                                                                     ptp4l[96541.125]: port 1: delay timeout
ptp4l[96199.580]: master offset 814429228950188135 s2 freq
                                                               -nan dath delay 814429218391406124
                                                                                                     ptp4l[96541.125]: delay filtered
                                                                                                                                                              5664
ptp4l[96199.581]: master offset 814429228951233895 s2 freq
                                                               -nan bath delay 814429218391406124
                                                                                                     ptp4l[96541.497]: port 1: delay timeout
```

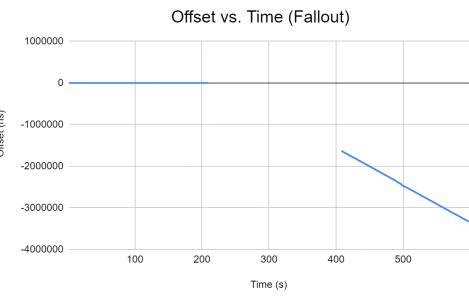




# **Clock Frequency Manipulation Attack Results**

- Clock servo algorithm unable to synchronize back to the source
- Source offset continues to drift even after the attack has concluded









#### **Timemaster**

- Timemaster is a lightweight service that incorporates a defense in depth strategy for timing synchronization
- NTP and PTP run simultaneously, with NTP acting as a fallback in case the PTP network goes unresponsive or is attacked
- Uses NTP as a fallback timing service if the PTP network is compromised
- All Timemaster testing was done using chronyd, an ntp daemon, and the linuxptp package

```
RL-PTP-Test3:~$ systemctl status timemaster
  timemaster.service - Synchronize system clock to NTP and PTP time sources
  Loaded: loaded (/lib/systemd/system/timemaster.service; disabled; vendor preset: enabled)
  Active: active (running) since Mon 2021-11-08 13:37:28 EST; 40s ago
    Docs: man:timemaster
 Main PID: 18243 (timemaster)
   Tasks: 4 (limit: 4915)
  Memory: 2.1M
     CPU: 22ms
  CGroup: /system.slice/timemaster.service
            -18243 /usr/sbin/timemaster -f /etc/linuxptp/timemaster.conf
            -18246 /usr/sbin/chronyd -n -f /var/run/timemaster/chrony.conf
            -18248 /usr/sbin/ptp4l -l 5 -f /var/run/timemaster/ptp4l.0.conf -H -i ens1f0
           -18255 /usr/sbin/phc2sys -1 5 -a -r -R 1.00 -z /var/run/timemaster/ptp41.0.socket -n 0 -E ntpshm -M 0
 ov 08 13:37:28 ECRL-PTP-Test3 ptp4l[18248]: [6667678.751] port 1: link up
 ov 08 13:37:28 ECRL-PTP-Test3 timemaster[18243]: [6667678.752] process 18255 started: /usr/sbin/phc2sys -1 5 -a -r
 ov 08 13:37:28 ECRL-PTP-Test3 ptp41[18248]: [6667678.768] port 1: link up
 ov 08 13:37:33 ECRL-PTP-Test3 chronyd[18246]: Selected source 50.205.57.38
     13:37:34 ECRL-PTP-Test3 ptp41[18248]: [6667684.780] port 1: new foreign master a0369f.fffe.1f62ac-1
  v 08 13:37:35 ECRL-PTP-Test3 ptp41[18248]: [6667686.285] selected best master clock a0369f.fffe.1f6748
 ov 08 13:37:38 ECRL-PTP-Test3 ptp4l[18248]: [6667688.780] selected best master clock a0369f.fffe.1f62ac
 ov 08 13:37:38 ECRL-PTP-Test3 ptp41[18248]: [6667688.780] port 1: LISTENING to UNCALIBRATED on RS SLAVE
 ov 08 13:37:41 ECRL-PTP-Test3 ptp4l[18248]: [6667691.780] port 1: UNCALIBRATED to SLAVE on MASTER CLOCK SELECTED
lov 08 13:37:55 ECRL-PTP-Test3 chronyd[18246]: Selected source PTP0
lines 1-24/24 (END)
```

Shows ECRL-PTP-Test1 getting selected by PTP as the source clock. Chronyd output shows PTP getting selected as the most accurate time source.

- Chrony selects PTP as the optimal time source, which is shown by the "\*" symbol. The "#" symbol identifies PTP as an internal time source. The last sample column shows the current offset, which is +/- 5ns.
- ECRL-PTP-Test1 (10.11.17.117) is advertised as the next most accurate time source as an NTP server.



#### **Timemaster Drawbacks**

- Unable to defend against passive attacks that manipulate a PTP network
- Most notably susceptible to the Source Clock Takeover attack and Covert Channels vulnerability since they do not produce large offsets while active in the a network

```
ECRL-PTP-Test3:~$ systemctl status timemaster
 timemaster.service - Synchronize system clock to NTP and PTP time sources
  Loaded: loaded (/lib/systemd/system/timemaster.service; disabled; vendor preset: enabled)
  Active: active (running) since Mon 2021-11-08 14:32:49 EST; 3min 26s ago
   Docs: man:timemaster
 Main PID: 19007 (timemaster)
   Tasks: 4 (limit: 4915)
  Memory: 2.4M
    CPU: 110ms
  CGroup: /system.slice/timemaster.service
           -19007 /usr/sbin/timemaster -f /etc/linuxptp/timemaster.conf
           -19012 /usr/sbin/chronyd -n -f /var/run/timemaster/chrony.conf
           -19014 /usr/sbin/ptp4l -l 5 -f /var/run/timemaster/ptp4l.0.conf -H -i ens1f0
           └─19015 /usr/sbin/phc2sys -l 5 -a -r -R 1.00 -z /var/run/timemaster/ptp4l.0.socket -n 0 -E ntpshm -M 0
lov 08 14:36:10 ECRL-PTP-Test3 ptp41[19014]:
   08 14:36:10 ECRL-PTP-Test3 ptp41[19014]
   08 14:36:10 ECRL-PTP-Test3 ptp4l[19014]:
   08 14:36:10 ECRL-PTP-Test3 ptp4[[19014]]
Nov 08 14:36:10 ECRL-PTP-Test3 ptp4[[19014]
   08 14:36:12 ECRL-PTP-Test3 ptp4l[19014]
Nov 08 14:36:13 ECRL-PTP-Test3 ptp41[19014]:
Nov 08 14:36:14 ECRL-PTP-Test3 ptp41[19014]
lov 08 14:36:15 ECRL-PTP-Test3 ptp41[19014]
 b@ECRL-PTP-Test3:~$ _
```

• PTP displays "port 1: received SYNC without timestamp" error messages, which is a normal occurrence during the source takeover attack

• Chrony still thinks that PTP is the most accurate time source, unaware of any compromise to the protocol.



# **Summary and Conclusions**

- PTP protocols are susceptible to a number of different attacks
  - Announce DoS
  - Source Spoof
  - Atomic Source Takeover
  - Correction Field MITM
  - Clock Frequency Manipulation
- INSTRUMENTATION
  AND MEASUREMENT

  A PUBLICATION OF THE HEE HOTTOMENTATION AND MEASUREMENT SOCIETY

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  MAY 2020 VOLUME 69 NUMBER 5 IEMAO

- Mitigation for the first three attacks has been proposed to the IEEE through IBM
- The last two attacks remain poorly understood
- Several covert channel options were also identified
- Ongoing investigations into PTP time cybersecurity

